

MAC'18: 18<sup>th</sup> Meeting of the FAIR Machine Advisory Committee

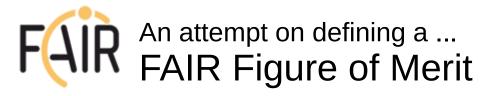


## FAIR Commissioning & Control WG

### 3-Year Status, Strategy & Concepts

(more details at: https://fair-wiki.gsi.de/FC2WG)

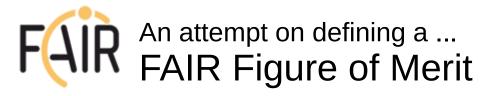
Ralph J. Steinhagen, S. Reimann for the FC<sup>2</sup>WG





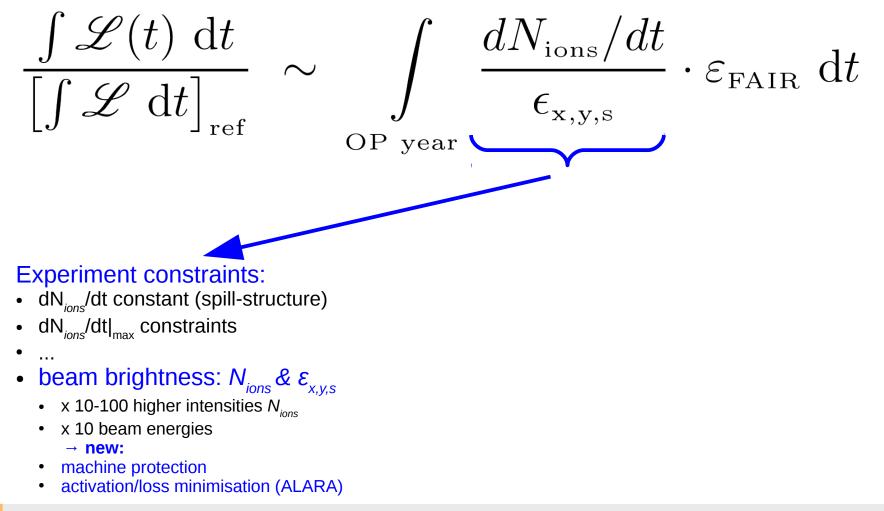
• Integrated luminosity/particle flux per experiment

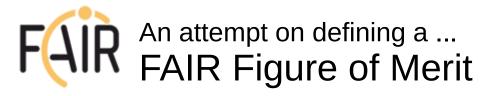
$$\frac{\int \mathscr{L}(t) \, \mathrm{d}t}{\left[\int \mathscr{L} \, \mathrm{d}t\right]_{\mathrm{ref}}} \sim \int \int \frac{dN_{\mathrm{ions}}/dt}{\epsilon_{\mathrm{x,y,s}}} \cdot \varepsilon_{\mathrm{FAIR}} \, \mathrm{d}t$$





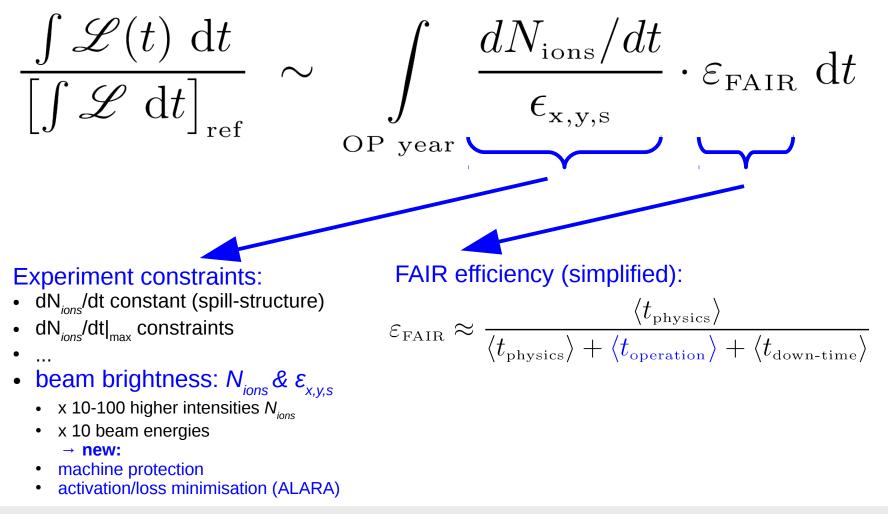
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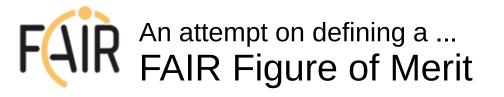






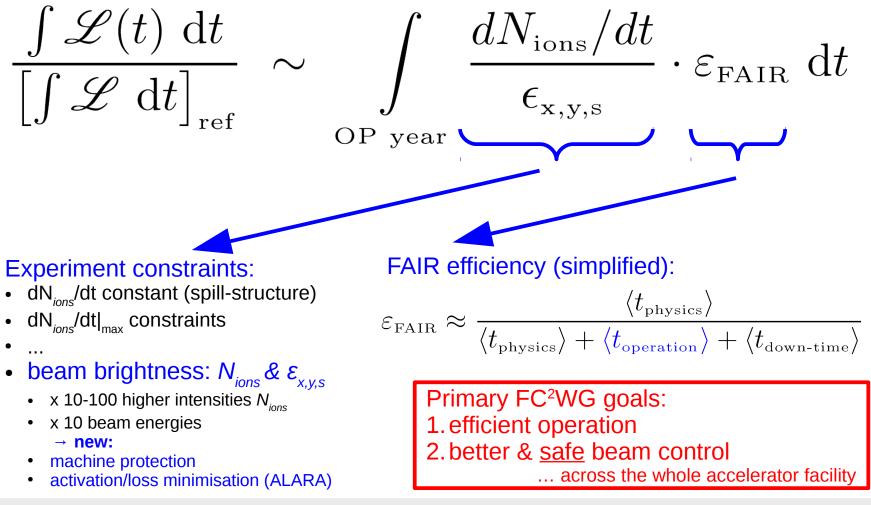
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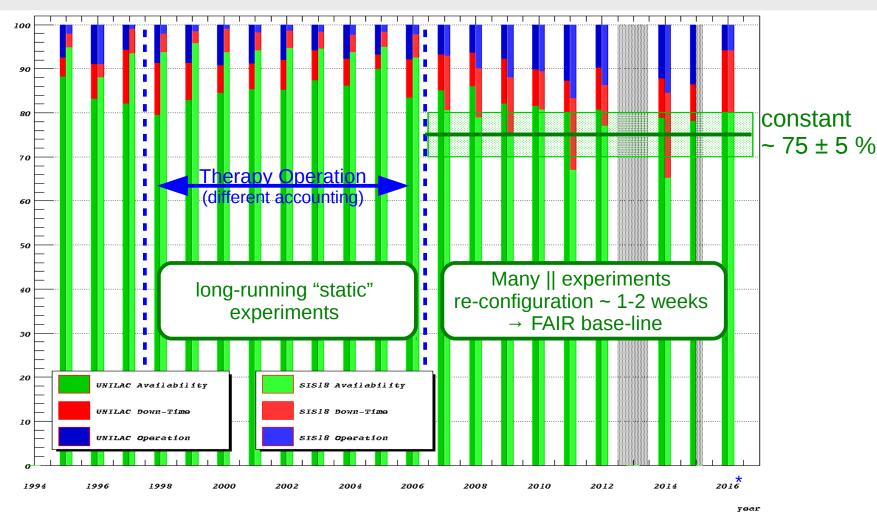


Integrated luminosity/particle flux per experiment



### FAR Accelerator Experience & Efficiency 1995-2016: U. Scheeler, S. Reimann, P. Schütt et al.





Based on: U. Scheeler, S. Reimann, P. Schütt et al., "Accelerator Operation Report", GSI Annual Scientific Reports 1992 – 2015 + 2016 (D. Severin) https://www.gsi.de/en/work/research/library\_documentation/gsi\_scientific\_reports.htm N.B. ion source exchanges are factored out from UNILAC & SIS18 data (~ constant overhead) Availability: *experiments* + *detector tests* + *machine development* + *beam to down-stream accelerators;* Down-time: *unscheduled down-time* + *standby;* Operation: *accelerator setup* + *re-tuning* 

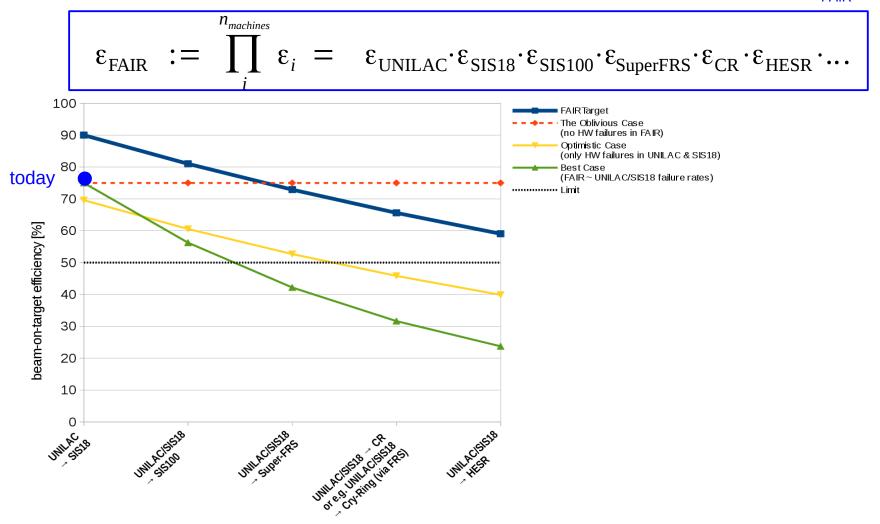
GSI Helmholtzzentrum für Schwerionenforschung GmbH Ralph J. Steinhagen, r.steinhagen@gsi.de, 2017-11-01

\* 2018 operation limitations:
• only ½ UNILAC (w/o A3 & A4)
• only 1 element in SIS18





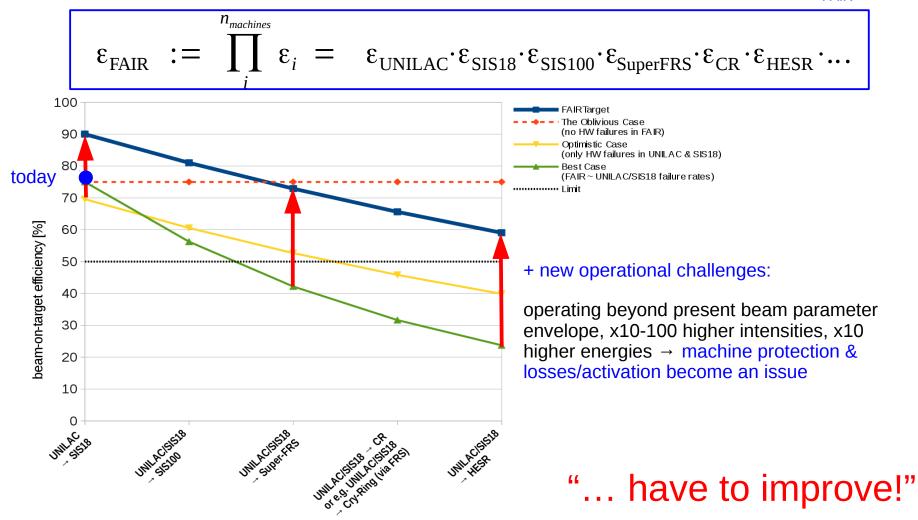
• Beam-on-Target figure of merit (FoM) of ~75%  $\rightarrow$  FAIR-BoT (efficiency  $\varepsilon_{\text{FAIR}}$ ):



### FAIR Challenges & Constraints ... SIS18 Operation Experience & Efficiency



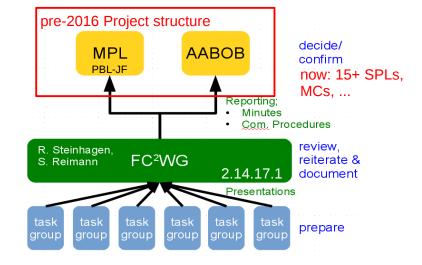
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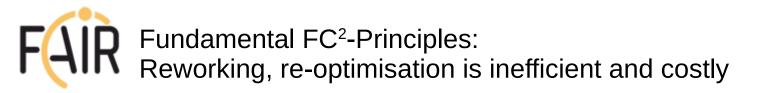
Scope: coordination, development and follow-up of a common and coherent concept, strategy, technical guide-lines, and specifications for the

- 1.Commissioning<sup>1</sup>,
- 2.Control<sup>2</sup>, and
- **3.Operation**
- of the whole<sup>3</sup> FAIR Accelerator Complex



- Broken down into 2 Work Packages:
  - 2.14.17.1 FC<sup>2</sup>WG -- FAIR Commissioning & Control WG (link to: 2.14.10.1, 2.x.[3,4,6,7 & 12])
    - development of common concepts, functional guidelines, and drafting of related specifications (acc. tech-experts)
      - support and tools for device and machine commissioning
    - EDMS signatory process:: SPL, MCs, main equipment-GL, head FC<sup>2</sup>WG (as approval leader)
  - 2.14.17.2 FCC-WG FAIR Control Centre WG (link to: 2.14.10.10)
    - technical control room infrastructure: consoles, furniture, IT infrastructure, etc. (mainly CO-IN, OP, Exp-reps.)
    - Machine-Civil-Construction Interface (↔ 'Campus Master Plan')

<sup>1</sup> commissioning covering the initial commissioning, subsequent re-commissioning & assisted operation phases (prior to 'regular operation')
 <sup>2</sup> N.B. 'control' here dt. "Anlagenkontrolle", including system integration and integration into day-to-day machine operation
 <sup>3</sup> including all existing GSI and future FAIR accelerators as well as machine-experiment specific CO interfaces (e.g. target steering, spill control, data exchange)





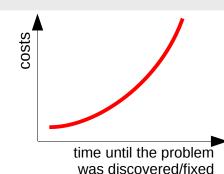
- Continuous improvement
  - Right processes to produce right results
     & for getting it right the first time
    - commissioning procedures as evolving operation standard
    - system integration: determine of what, how and when is needed

Fundamental FC<sup>2</sup>-Principles:

- Continuous improvement
  - Right processes to produce right results & for getting it right the first time
    - commissioning procedures as evolving operation standard
    - · system integration: determine of what, how and when is needed
      - N.B. lack of resources ↔ prioritisation!! (N.B. steering of resources by SPLs & MCs)

Reworking, re-optimisation is inefficient and costly

- Prevention of inefficiencies, inconsistencies & wastes by design
  - 'poka-yoke' or 'error proofing' principle culture of stopping and fixing
    - 1. early, when and where they occur (at the source)
    - 2. with low-intensity beam rather than with high-intensity beam



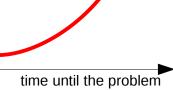


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    - addressing first basic parameters before complex higher-order effects
  - Examples:
    - now: semi-automated testing tools for individual devices now  $\rightarrow$  later: time savings for largescale FAIR SATs
    - first fix injection, trajectory, orbit, Q/Q' before addressing space-charge or slow-extraction problems
    - important losses for low-intensity beam have larger impact for high-intensity beam (↔ activation)
      - pilot-beam concept: <u>always</u> verify machine safety with low-intensities before moving on to high-intensity beams





costs



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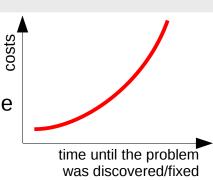
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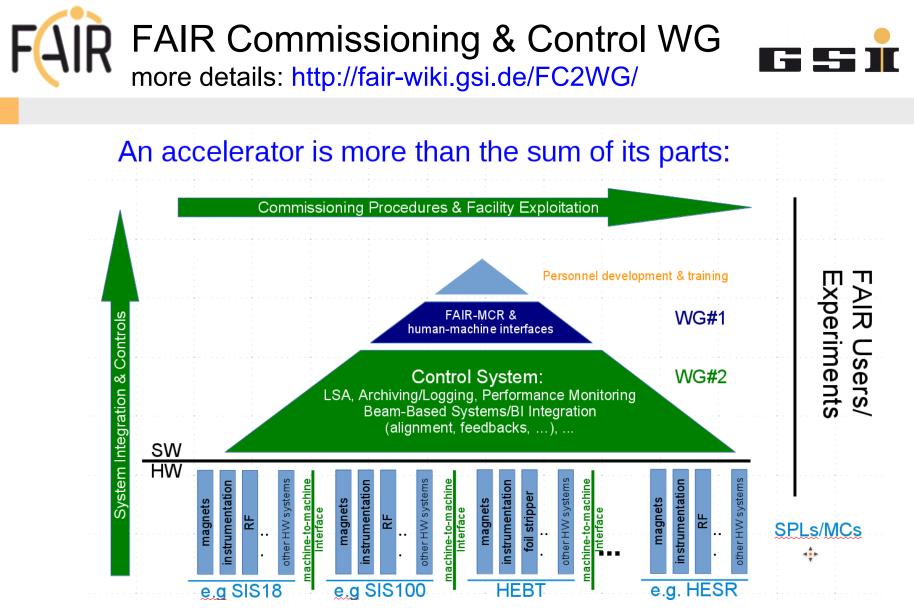
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#### • Respect for people - "develop people, then [/and] build products [/accelerators]"

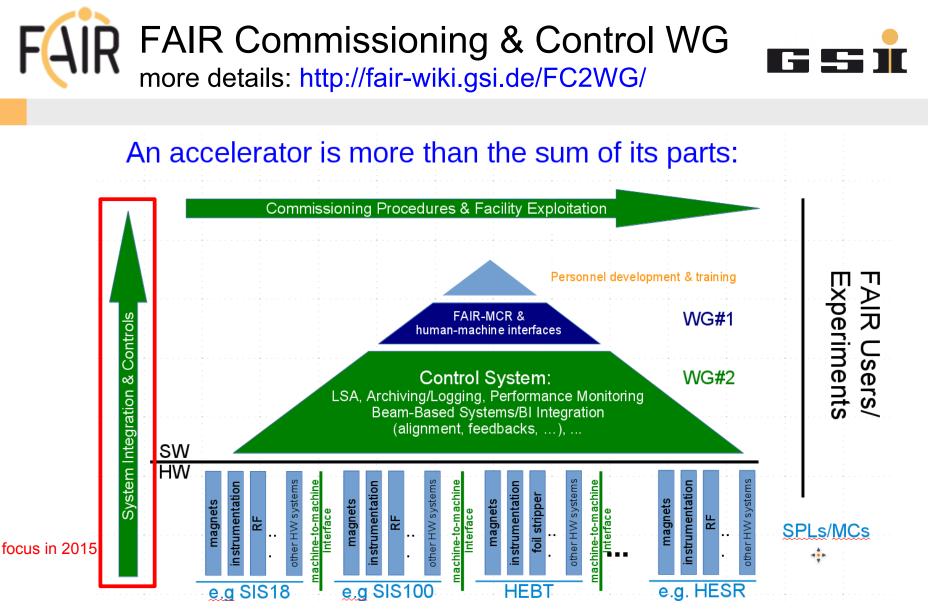
- optimise operation ↔ smart tools & procedures, e.g. beam-based feedbacks, sequencer, ...
  - automate routine task so that operator talents are utilised and focused on more important tasks
- training, investment in and development of people minimise overburden/strain of personnel
- FAIR is a large facility and needs wider support: communicate concepts and strategy to broader base  $\rightarrow$  FC<sup>2</sup>WG



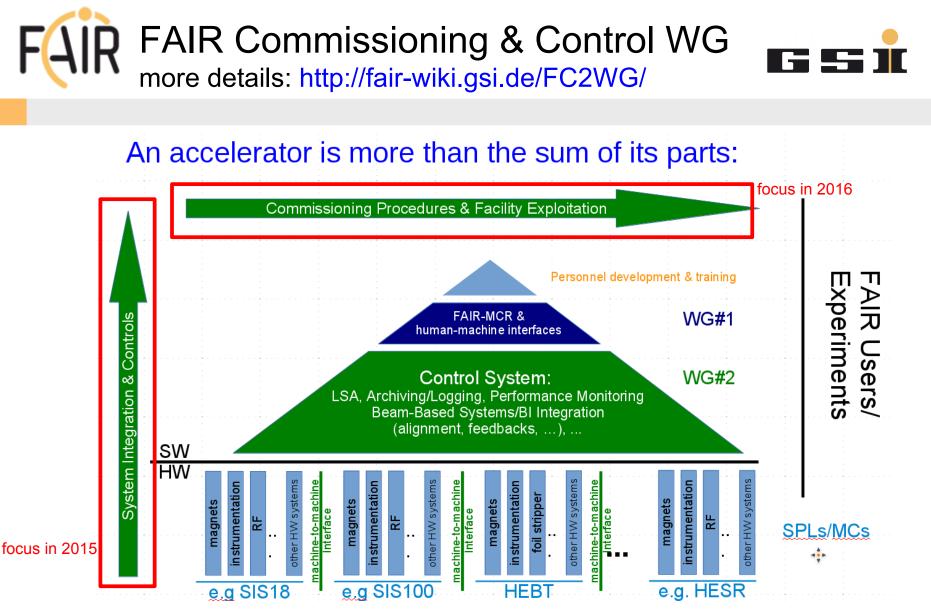




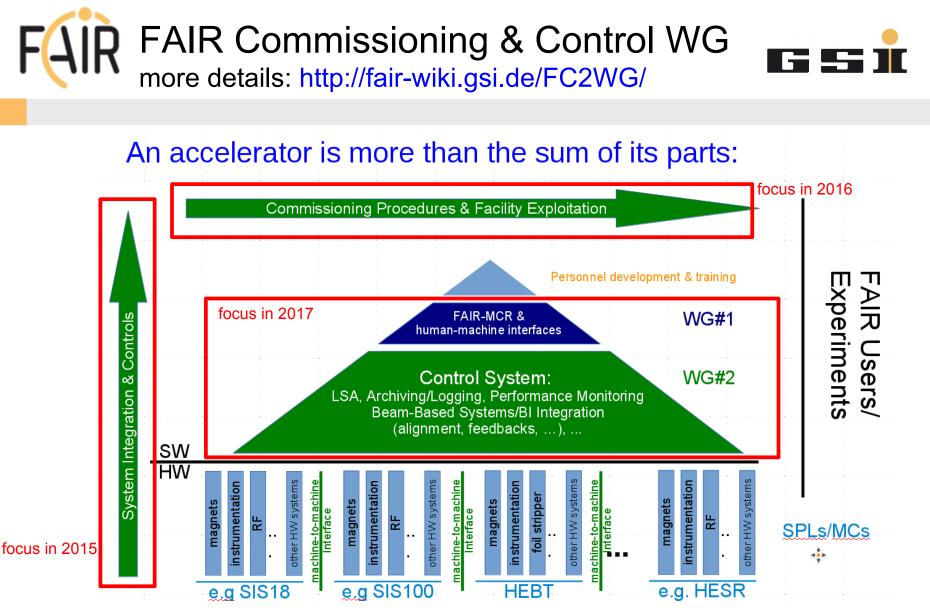
 FAIR Commissioning & Control Working Group platform to identify, coordinate, and work-out FAIR commissioning and operation
 open to all who can participate and contribute to these subjects!



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### FAR FC<sup>2</sup>WG Topics – more than "Control System" & Data Supply

primary aim: provide tools, extensions to, and integration of the existing basic technical system to ensure a swift, efficient commissioning and control of the accelerator facility

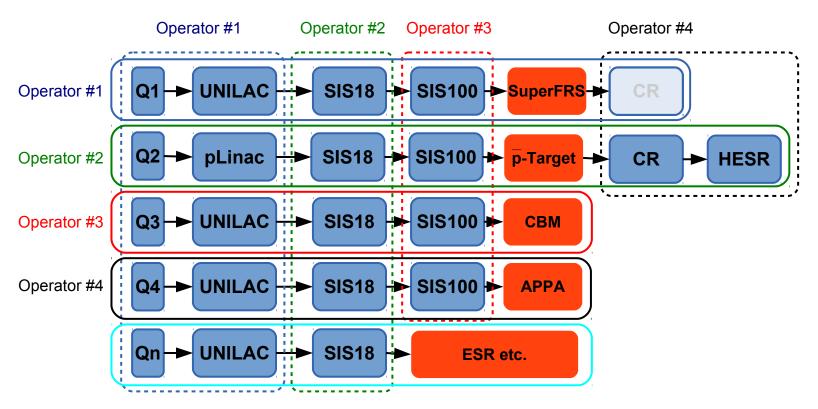
- Facility & Interface Analysis
  - Procedures: Hardware Commissioning (HWC), HWC-'Machine Check Out', Beam Commissioning (BC), BC-Stage A (pilot beams), BC-Stage B (intensity ramp-up), BC-Stage C (nominal/production operation) Beam parameters, FAIR performance model, optimisation, Accelerator & Beam Modes
- Beam Instrumentation & Diagnostics System Integration (into operation and controls environment)
  - Intensity (DCCTs) & beam loss (BLMs) → Beam Transmission Monitoring System (BTM), trajectory & orbit (BPMs), Q/Q', optics (LOCO & phase-advance), longitudinal & transverse emittance (FCTs, WCM, screens, IPM, etc.), Δp/p, long. bunch shape (FCTs, Tomography), abort gap monitoring, ...
- Accelerator Hardware System Integration (into operation and controls environment)
  - Power converter, magnets, magnet model, RF, injection/extraction kicker, tune kicker/AC-dipole, beam dump, collimation/absorbers, cryogenics, vacuum, radiation monitoring, k-modulation, technical infrastructure (power, cooling/ventilation), machine-experiment interfaces
- Control System
  - Archiving system, acquisition/digitization of analog signal, test-beds, timing, bunch-to-bucket transfer, cyber security, rolebased-access, middleware, real-time & cycle-to-cycle feedbacks, daemons
- Components
  - post-mortem, management of critical settings (safe-beam settings), machine protection, interlocks, beam quality checks, daemons, 'facility status display', aperture model, ...
- Applications
  - Sequencer (semi-automated test/commissioning procedures), fixed-displays, ...
  - Beam-Based Applications & GUIs

topic started topic active topic not started

# FAIR Operation/Control Paradigm



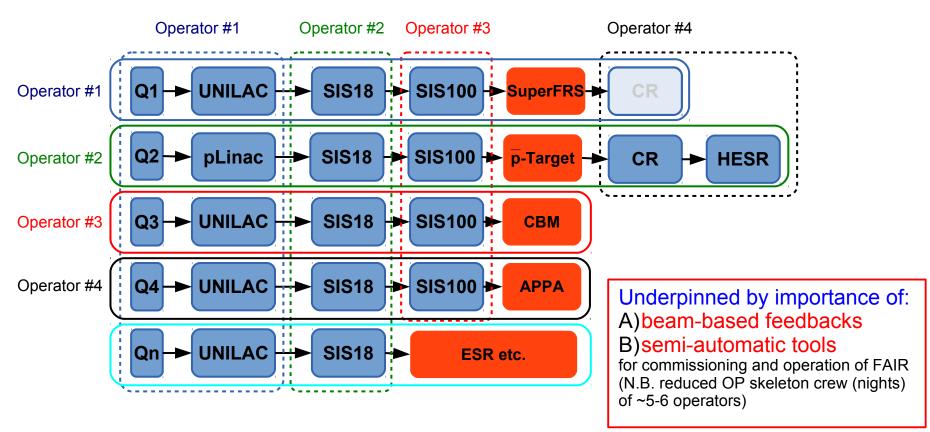
- Some important OP boundary conditions:
  - A) Compared to GSI, FAIR facility size and complexity increases roughly by a factor 4
  - B) Expect some improvement but 'Operator' & 'System Expert' will likely remain a scarce resource
- One strategy item: 'One Operator per Accelerator Domain' vs. 'One Operator per Experiment':



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- Purpose:
  - Communication of intended accelerator operation to experiments, FAIR and wider community
    - · what to expect and when, beam time performance tracking & analysis
  - Conditioning of control sub-system responses
    - e.g. logging, interlocks, management of critical settings (MCS & RBAC), machine sequencer, access system, ...
    - · associated rules of what is allowed, when, where etc. e.g.:
      - Limit parameter changes during data taking aka. 'Stable Beams'/'Production Runs'
      - No high-intensity beam injected into an 'empty' machine
      - Block certain operation during unsafe mode of operation



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- Main modes:
  - 1) Accelerator (Machine) Modes
    - · covering rule sets outside of beam operation
    - defined per accelerator/transfer-line segment

#### 2) Beam Modes

- · covering rule sets during beam operation
- · defined per accelerator/transfer-line segment and beam-production-chain
- · proposed: extend this concepts also to experiment targets
  - required for safe primary-beam intensity ramp-up & 'OP-Exp. Hand-Shake' etc.
  - more fine-grained options for facility availability, performance tracking & analysis



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Quality Management	Document Type:	Document Number: F-CS-B-0003e	Date: 2017-09-21
FÁIR	Common Specification	Template Number: Q-FO-QM-0005	Page 1 of 15

Ocument Title:	Accelerator and Beam Modes
Description:	Common Specification, definition and integration of Accelerator Modes and Beam Modes into accelerator facility operation and control system
Division/Organization:	FAIR/GSI
field of application:	FAIR Project, existing GSI accelerator facility
/ersion	V 1.1

#### ostract

his common specification outlines two fundamental modes: the 'accelerator mode' covering rule ets and operational sequence outside of beam operation and that are defined per accelerator or earn-line section (e.g. shutdown, setup, brykiss run, etc.); and the 'beam mode' covering rule sets uring beam operation and that are defined per accelerator or beam-line section <u>and</u> Beamroduction-Chanic (e.g. no beam, pitol beam, stable beam, etc.).

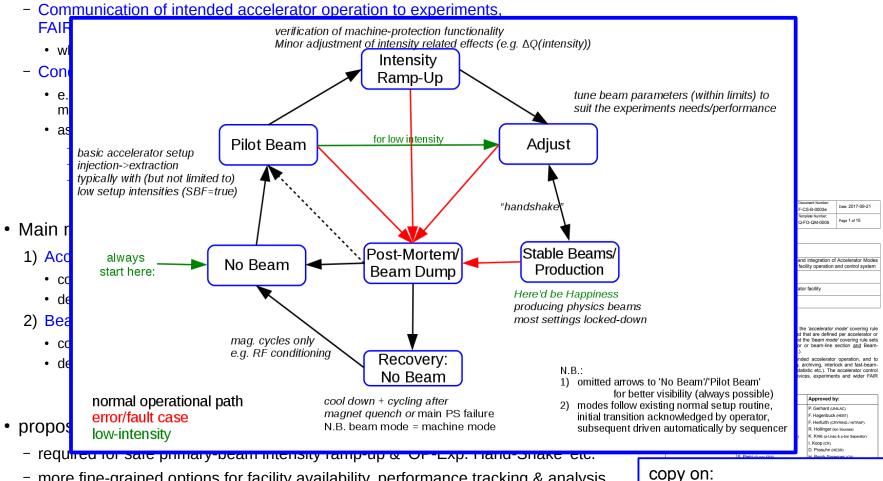
The purpose of these modes is to communicate the intended accelerator operation, and to condition the various accelerator sub-system responses (e.g. archiving, interiock and fast-beam body system, smagement of critical settings, beam time statistic etc.). The accelerator control system will distribute this information to the accelerator devices, experiments and wider FAIR community.

Prepared by:	Checked by:	Approved by:
R. Steinhagen	FAIR-C2WG-ALL (e-group)	P. Gerhard (UNILAC)
FAIR Comm. & Control PL)	J. Fitzek (Controls)	F. Hagenbuck (HEBT)
R. Bär (Controls)	F. Gressier (QA)	F. Herfurth (CRYRING / HITRAP)
	S. Jülicher (Controls)	R. Hollinger (Ion Sources)
	I. Lehmann (experiment link-person)	K. Knie (p-Linac & p-bar Separator)
	C. Omet (SIS-100 MP)	I. Koop (CR)
	D. Ondreka (System Planning)	D. Prasuhn (HESR)
	S Pietri (Super EDS)	H Reich-Sprenger (CS)

#### copy on: https://fair-wiki.gsi.de/FC2WG/



Purpose:



- more fine-grained options for facility availability, performance tracking & analysis

https://fair-wiki.gsi.de/FC2WG/

### FAIR Archiving System https://edms.cern.ch/document/1176039/



- ... collect and store all pertinent accelerator data centrally to facilitate the analysis and tracking of the accelerator performance as well as its proper function.
- Combined Archiving and Post-Mortem storage concepts
- Aim at storing maximum reasonable amount of data
  - facilitates data mining (performance trends, rare failures, ...)
  - key to understanding and improving accelerator performance
- Milestones:
  - Conceptual prototype for 2016/17 (in-kind)
  - Aim at testing this for > 2018

#### Archiving



#### Post-Mortem



Quality Management	Document Type:	Document Number: F-DS-C-11e	Date: 2016-07-18
FÁİR	Detailed Specification	Template Number: Q-FO-QM-0005	Page 1 of 24
Document Title:	Document Title: Detailed Specification of the FAIR Accelerator Control System Component "Archiving System"		
Description:	This document is the Detailed Specification of the accelerator control system component 'Archiving System'. Its task is to collect and store all pertinent accelerator data centrally to facilitate the analysis and tracking of the accelerator performance as well as its proper function.		
Division/Organization:	CSCO	CSCO	
Field of application:	FAIR Project, existing GSI accelerator facility		
Version	V 4.5		

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	J. Fitzek (CO)	
	S. Reimann (OP)	
	P. Schütt (OP)	
	C. Omet (SIS-100 MP)	
	D. Ondreka (System Planning)	
	I. Lehmann (Machine-Exp.)	
	D. Severin (Machine-Exp.)	
	MPLs & MCs*	

\*List of Machine-Project-Leaders (new FAIR acc.) & Machine Coordinators (existing facility): F. Hagenbuck (HEBT), R. Bár (Controls), M. Winkler (Super-FRS), O. Dolinskyy (CR), P. Spiller (SIS-100), K. Knie (p-Linac & p-bar Separator), R. Steinhagen (FAIR Comm. & Control), R. Hollinger (Ion Sources), P. Gerhard (UNILAC), J. Stadimann (SIS-18), M. Steck (ESR), F. Herfurth (CRYRING / HITRAP).

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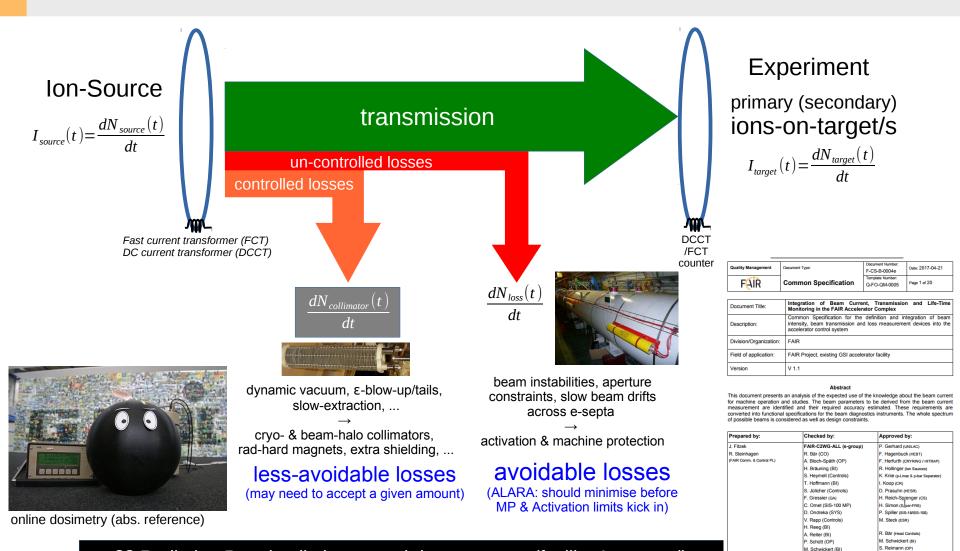
Quality Management	Document Type:	Document Number: F-DS-C-11e	Date: 2016-07-18
FÁİR	Detailed Specification	Template Number: Q-FO-QM-0005	Page 1 of 24
Document Title:	Detailed Specification of the FAIR Accelerator Control System Component "Archiving System" This document is the Detailed Specification of the accelerator control system component 'Archiving System'. Its task is to collect and store all pertinent accelerator data centrally to facilitate the analysis and tracking of the accelerator performance as well as its proper function.		
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Version			

Prepared by:	Checked by:	Approved by:
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N.B. importance: quantitative accelerator performance and bug/fault-tracking indicators

Beam Transmission Monitoring (BTM) https://edms.cern.ch/document/1823362/





§§ Radiation Permit – limits on total dose per year (facility & external)

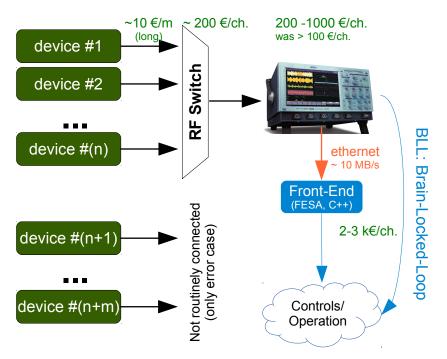
I. Lehmann (Exp. Link-Person) D. Severin (Exp. Link-Person) R. Steinhagen

AIR Comm. & Control PL



• traditional/old concept

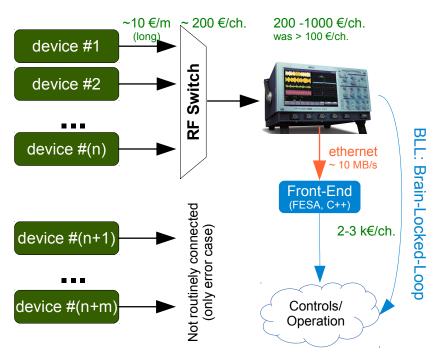
(underlying assumption: scopes/digitizers are expensive, RF switches are cheap)





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#### on-demand measurement

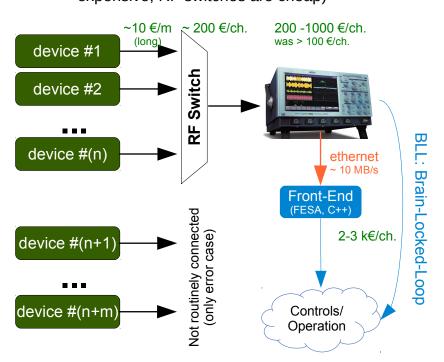
(selected signals, error-case, ...)

#### con:

- high-reconfiguration overhead (manual)
- limited test-coverage, trending



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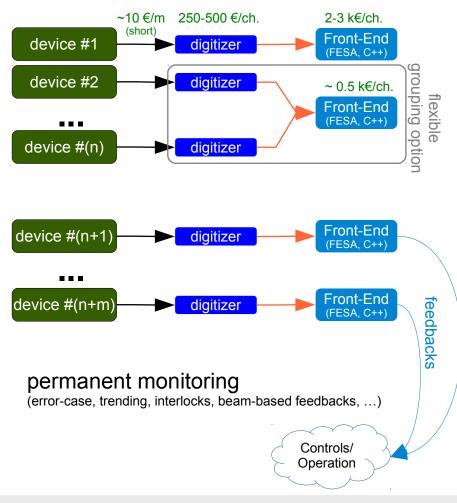
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GSI Helmholtzzentrum für Schwerionenforschung GmbH

#### targeted concept

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	Quality Management	Document Type:	Document Number: F-CS-C-0002e	Date: 2017-09-21		
	FÁİR	Common Specification	Template Number: Q-FO-QM-0005	Page 1 of 29		
d d	Document Title: On the Digitization of (generic) Analog Signals in the Accelerator Complex		nals in the FAIR			
	Description:	Detailed specification for the integration of time-domain digitizers with analog bandwidths and sampling frequencies ranging from DC to up to hundreds of MHz into the accelerator control system				
	Division/Organization:	FAIR				
	Field of application:	FAIR Project, existing GSI accelerator facility				
de	Version	V 1.1				

#### Abstract

This document describes the generic integration of time-domain digitizers with analog bandwidths and sampling frequencies ranging from a few MHz to hundreds of MHz. These digitizers shall provide generic monitoring and diagnostics of accelerator-related devices that otherwise do not require further dedicated IO control features, specific post-processing (e.g. fast feedback loops), or where these features are already handled through another existing infrastructure.

This specification aims at providing a generic abstraction of the vendor-specific digitizer software interfaces, a limited range of generic data post-processing on the acquired data, and integration of these devices into the FAIR control systems by providing FESA standardised software interfaces.

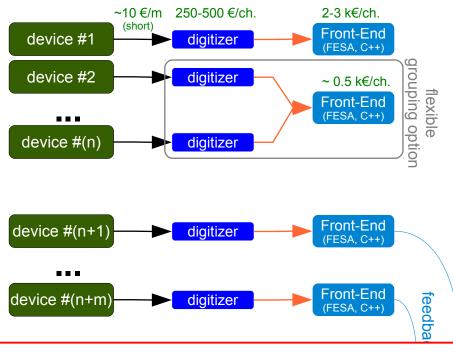
de

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#### limited test-coverage, trending

targeted concept

(underlying assumption: scopes/digitizers are cheap, RF switches are expensive)



start deployment  $\geq$ 2018 (SIS18), crucial for:

- migration to new FAIR Control Centre (FCC),
- optimisation of commissioning & operation
- tracking/isolation of faults (↔ post-mortem)
- less-biased performance indicator

#### link: more details

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## FAIR Commissioning Procedure I/II $\mathbf{E} = \mathbf{I}$

- Develop a (initial/re-)commissioning and operation strategy:
  - memorandum of understanding between stake-holders (SPLs, MCs, AP, BI, CO, RF, ...)
    - define when, where and how the individual accelerator systems should fit in
  - identify and define missing procedures, equipment and tools, e.g.:
    - define, check the need or priority of applications vs. 'use cases'
    - define, check integration and interface between specific equipment and CO/OP environment
  - focus first on commonalities across then specifics within individual accelerators
    - MPLs/MCs define pace & resources of how fast to achieve the above
  - dissemination/knowledge transfer from groups that constructed and performed the initial HW commissioning to the long-term operation
    - training of operational crews (physics, operation, tools, ...)
  - scheduling tool for technical stops to follow-up possible issues found

\*Procedure aims not only at the initial first but also subsequent re-commissioning e.g. after (long) shut-downs, mode of operation changes and/or regular operation

### FAIR Commissioning Procedure II/II https://fair-wiki.gsi.de/FC2WG/HardwareCommissioning/



- Distinguish two forms of 'commissioning':
  - A)Hardware Commissioning (HWC  $\rightarrow$  SAT A)
    - conformity checks of the physical with contractual design targets,
    - || commissioning of individual systems & tasks ↔ MPLs/equipment group responsibility
  - B)Commissioning with Beam (BC  $\rightarrow$  "SAT B" ... )
    - Commissioning of beam-dependent equipment
    - Focus on tracking beam progress through the along the beam production chain (BPC)
      - threading, injection, capture, acceleration and extraction
    - + 'Dry-Runs': pre-checks at the end of HWC in view of beam operation:
      - Checks conformity of system's controls integration and readiness for Commissioning with Beam
      - check as much control/system functionality without beam as possible
      - Machine ist put into a state assuming that beam could be injected into the ring/segment
        - unavailable devices/systems are at first ignored, noted down, and followed-up at a defined later stage

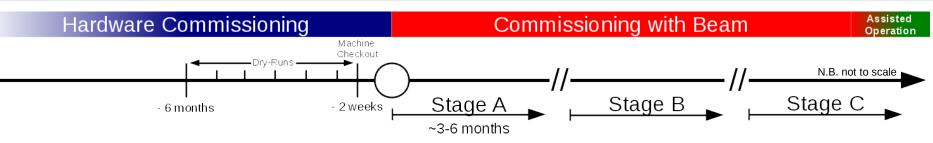
#### Terminology:

- Dry-runs: a rehearsal of the accelerator performance/function, starting typically six month before the targeted real BC
  - needs to (partially) repeated after shut-down or longer technical stop with substantial modifications
  - initial frequency: 1-2 days every month
  - frequency increased depending on the outcome of the initial dry-run tests
- Machine-Checkout: intense accelerator performance tests (e.g. machine patrols, magnet/PC heat runs, etc.), typically two weeks before BC
  - needs to repeated after every shut-down or longer technical stop
  - repeated also on the long-term during routine operation of existing accelerators (already existing procedures/usus for existing machines)

### Commissioning without & with Beam

https://fair-wiki.gsi.de/FC2WG/HardwareCommissioning/ https://fair-wiki.gsi.de/FC2WG/BeamCommissioning





Split Beam Commissioning into three stages:

#### A) Pilot beams/"easily available" ions (e.g. Ar)

- basic checks: threading, injection, capture, cool, convert, acceleration/decelerate, stripping & extraction
- · always done with 'safe' ie. low-intensity/brightness beam
  - Ions: simpler optics, beam dynamics  $\rightarrow$  Protons: transition crossing

#### B) Intensity ramp-up & special systems

- · achieving and maintaining of nominal transmission and beam losses
- commissioning of e.g. e-cooler, slow extraction, transverse fast feedbacks
- · commissioning and validation of machine protection & interlock systems
- · Possibly unsafe operations always preceded by checks with safe beam

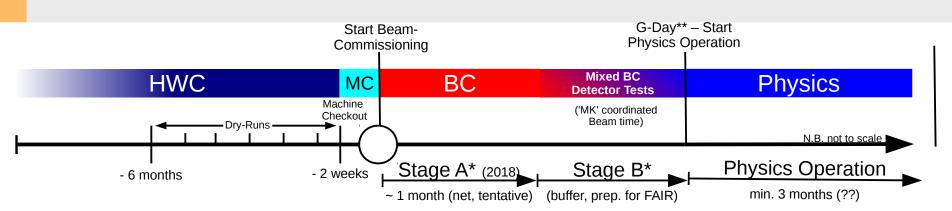
#### C)Production operation with nominal intensities

(N.B. first time counted as 'commissioning' or 'assisted operation'  $\rightarrow$  later: 'regular operation')

- push physics and beam parameter performance (emittance, momentum spread, ...)
- · identify and improve upon bottlenecks impacting FAIR's 'figure-of-merit
- make fast setup and switch-over between different beam production chains routine

### Recommissioning & Operation in 2018 I/II https://fair-wiki.gsi.de/FC2WG/HardwareCommissioning/





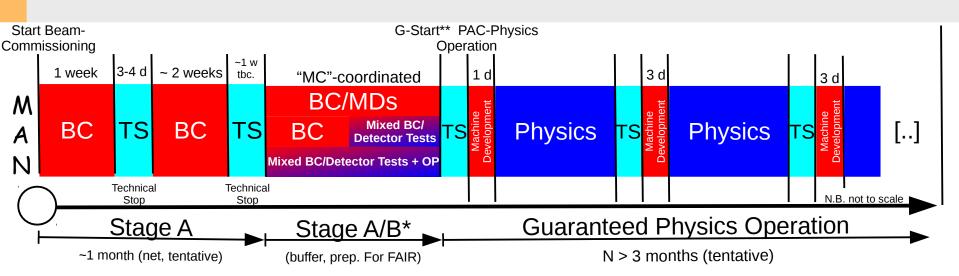
- Hardware Commissioning  $\rightarrow$  coordinated by Sub-Project-Leaders & Machine Coordinators
  - link-existing facilities (GAF), upgrades, machine re-alignment, "SATs", HW systems (equip. groups),
- Dry-Runs for all machines post (possibly also UNILAC), each two days, fixed dates (↔ experts availability), starting:
  - Dry-Run #1 25.10.2017: CO-core: LSA, Timing System, Archiving System, SCUs, CO core application, ...
  - Dry-Run #2 14.11.2017: as before + tbd.
  - Dry-Run #3 12.12.2017: as before + tbd.
  - Dry-Run #4 09.01.2018: as before + BI + related applications
  - Dry-Run #5 06.02.2018: as before + Experiments (proposal) + Machine-Experiment Interfaces
  - Dry-Run #6 20.02.2018: as before + AEG + "last-minute" checks
  - Dry-Run #7 06.03.2018: buffer
- Machine-Checkout intensive "last minute checks" (N.B closed tunnel/machine):
  - UNILAC: -1 month  $\rightarrow$  BC- 'day 0'
    - patrols, heat runs: RF & power supply conditioning, ...
  - SIS, ESR, CRY: -3 weeks  $\rightarrow$  BC- 'day 0'
    - patrols, heat runs: RF & power supply/AEG conditioning, safety systems: personnel safety, access system, legal ZKS & RP checks (§66 Abs. 2 StrlSchV), "very last-minute" checks/bug fixes: vacuum, power, BI, CO, ...

<sup>\*</sup> in 2018: light-version w.r.t. commissioning of new machines

<sup>\*\* &</sup>quot;guaranteed" start physics operation (Plan A), no hick-ups, sacrificial buffer being activities related to 'Stage B'

### FAR Recommissioning & Operation in 2018 II/II https://fair-wiki.gsi.de/FC2WG/BeamCommissioning





- Stage-A: Initial Beam Commissioning (BC): 2 dedicated 3 week@24h/7 BC blocks, main aim:
  - drive beam expeditiously through the Beam Production Chain: sources  $\rightarrow$  synchrotrons & beam transfers  $\rightarrow$  exp. targets & storage rings
  - check basic 'accelerator mechanics': threading, injection, capture, cool, convert, acceleration/decelerate, stripping & extraction
  - identify beam-related limitations: polarities, RF, beam instrumentation, machine alignment, effective physical machine aperture, ...
- Immediately followed by dedicated, scheduled Technical Stop (TS)
  - needed for follow-up of HW (tunnel) and SW issues (CO, ...)
- Stage-A/B\*: Mixed-BC, Machine-Development, Detector Tests (aka. "splash events" for experiments) & Operator Training
  - N.B. "old machine" but completely new CO, substantial modifications
- Physics operation: as promised/targeted nett 3 months (to be confirmed), grouped into 2-3 blocks interleaved with
  - TS: routine maintenance → increases overall availability, follow-up of OP/CO/equipment issues + major ion species/source changes
  - MDs: follow-up of beam physics issues, CO improvements (e.g. beam-based FBs), improve facility to reach nominal FAIR parameters
    - N.B. also better for guaranteeing smooth restart/picking-up of physics operation after technical stops (experts availability)

### FAIR Control Centre (FCC) https://edms.cern.ch/document/1821654/



#### Requirements & Conceptual Design – primary goals:

- provide sufficient room for the operation of the existing and enlarged GSI/FAIR facility
- ergonomics: Main Control Room should not "get in the way of it's primary function"
  - establish functional relationships between MCR & ancillary rooms
  - validate/check w.r.t. FAIR Commissioning & Control concept
  - validate/check whether input for building planner is feasible and consistent with DIN/ISO norms



N.B. surface numbers are approximate and may change due to civil construction constraints outer hull measurements, colour, design, etc. may change

••••

Lower floor: showers, changing rooms, lockers, CSCO-IN infrastructure, building infrastructure, ...

R. J. Steinhagen

### FAIR Control Centre (FCC) https://edms.cern.ch/document/1821654/



#### Requirements & Conceptual Design – primary goals:

- provide sufficient room for the operation of the existing and enlarged GSI/FAIR facility
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N.B. surface numbers are approximate and may change due to civil construction constraints outer hull measurements, colour, design, etc. may change

#### N.B. FCC "ready" for HWC starting 2022 (+ backup option)

Quality Management	Document Type:	F-CS-B-0002e	Date: 2017-06-21
F <mark>A</mark> İR	Common Specification	Template Number: Q-FO-QM-0005	Page 1 of 25
Document Title:	Common Specification for th Commissioning, Operation, a FAIR Accelerator Facility		
Description:	Functional requirements, ergonomics, and design of the FAIR (FCC) covering: FCC Main Control Room (MCR), MCR-relate conference rooms, visitor's gallery, and ancillary infrastructure.		R-related meeting and
Division/Organization:	FAIR/GSI		
Field of application:	FAIR Project, existing GSI accel	erator facility	

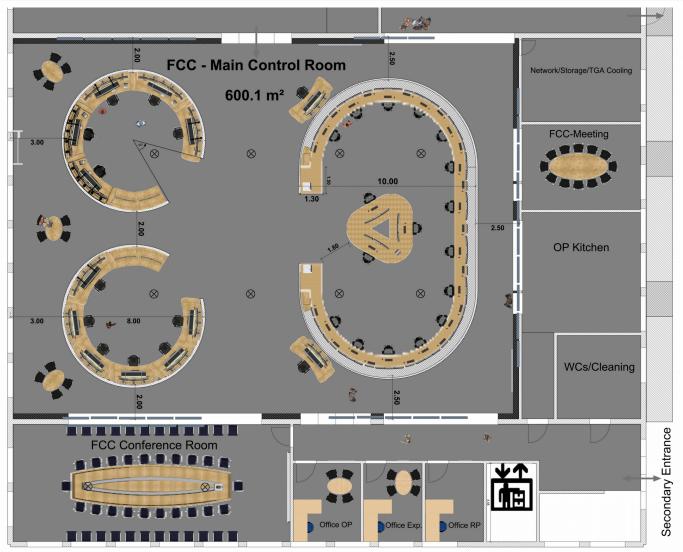
#### Abstract

This document describes the user-level functional requirements, ergonomics considerations, and derived design of the FAR Control Center (FCC) from an accelerator commissioning, operation, and operational exploitation point of view, including experiments that are tightly intertwined with accelerator operation. This specification builds upon best practises and operational experiences with similar, existing accelerator infrastructures at GSI, CERN and other large international aboratories, and summarises the present user-community understanding, discussions and ergonomics in view of the future operation of FAR. This document extends, combines, and supersedes previous FAR specifications FJ-DS-C21e and F-DS-C22e.

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all surface areas are approximate

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## Appendix

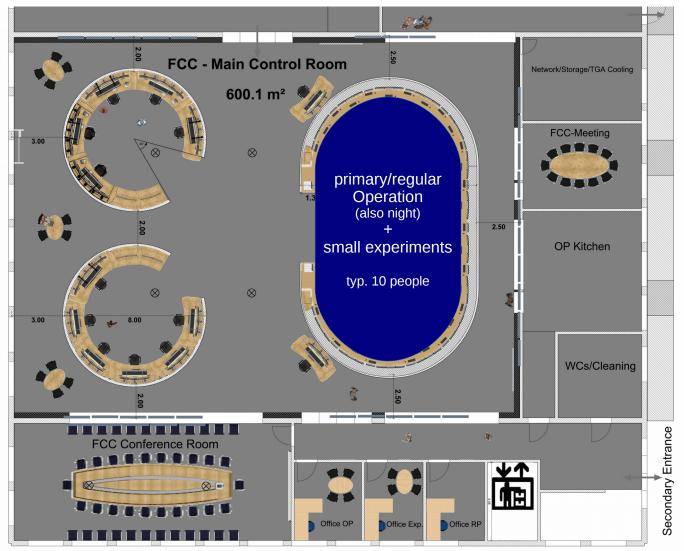


### FAIR, can we do it?



### Yes, we can!

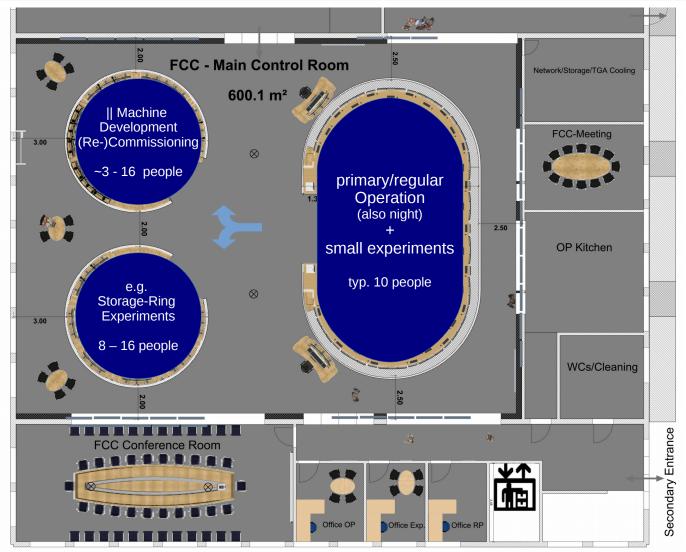




all surface areas are approximate

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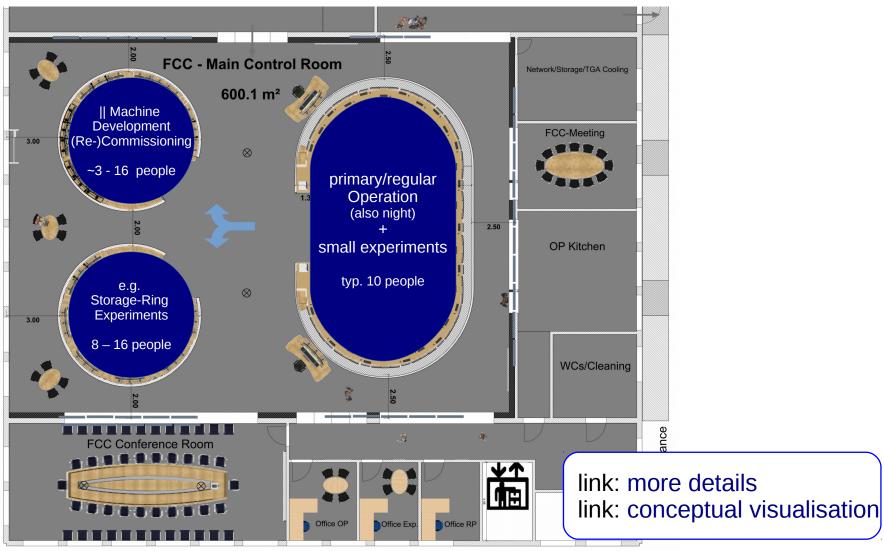




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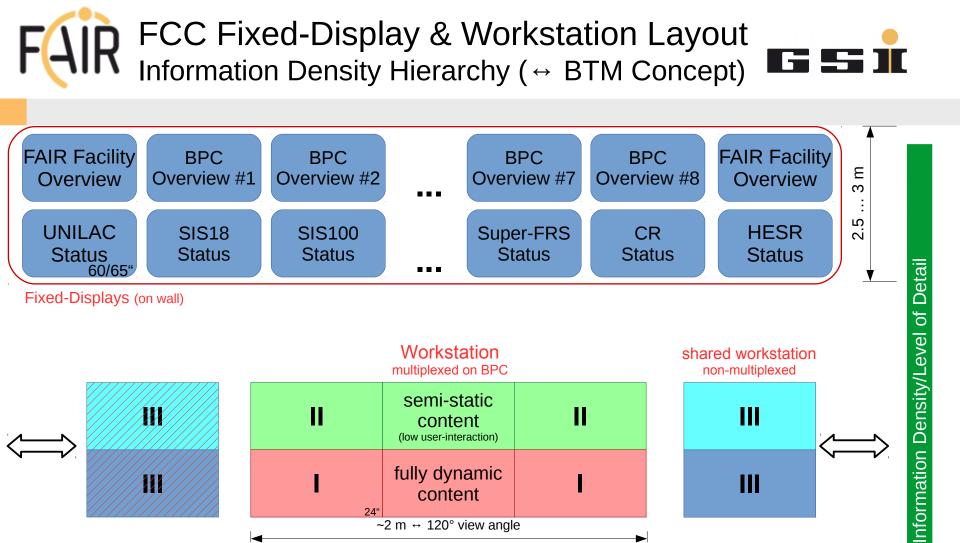




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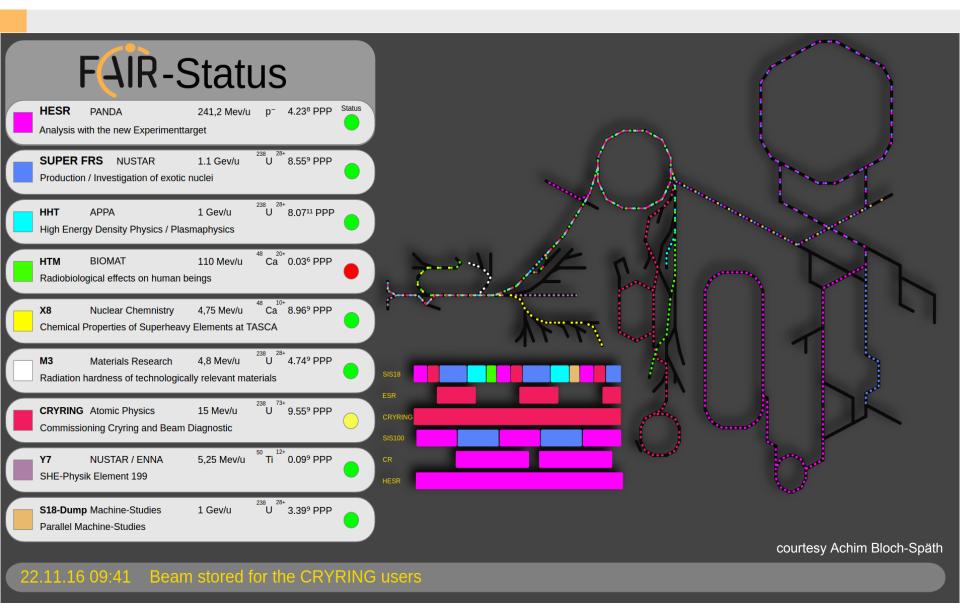
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Ralph J. Steinhagen, r.steinhagen@gsi.de, 2017-11-01



- I: semi-fixed displays monitoring context, rare interactions (slightly overhead)
  - beam-transmission/beam-loss monitoring, emittance preservation, primary experiment performance index, ...
- II: active user-interaction automatically adapted to commissioning step (see FC<sup>2</sup>WG)
- III: non-multiplexed information:
  - Zugangskontrollsystem' (ZKS, access system), machine interlocks, ...

FAR FCC Fixed-Display & Workstation Layout Information Density Hierarchy (↔ BTM Concept)



### FAR FCC Fixed-Display & Workstation Layout Information Density Hierarchy (↔ BTM Concept)



GSI Helmholtzzentrum für Schwerionenforschung GmbH Ralph J. Steinhagen, r.steinhagen@gsi.de, 2017-11-01